# Model Checking I alias Reactive Systems Verification 

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## Topics

- Transition Systems


## Material

Reading:
Chapter 2 of the book, pages 19-26.

More:

The slides in the following pages are taken from the material of the course "Introduction to Model Checking" held by Prof. Dr. Ir. Joost-Pieter Katoen at Aachen University.

## Overview

Introduction
Modelling parallel systems
Transition systems
Modeling hard- and software systems
Parallelism and communication
Linear Time Properties
Regular Properties
Linear Temporal Logic
Computation-Tree Logic
Equivalences and Abstraction

## Transition systems



## Transition systems


semantic model

## Transition systems



The semantic model yields a formal representation of:

## Transition systems



The semantic model yields a formal representation of:

- the states of the system
- the stepwise behaviour
- the initial states


## Transition systems



The semantic model yields a formal representation of:

- the states of the system
control component + information on "relevant" data
- the stepwise behaviour
- the initial states


## Transition systems $\widehat{=}$ extended digraphs



The semantic model yields a formal representation of:

- the states of the system $\longleftarrow$ nodes

- the stepwise behaviour $\longleftarrow$ edges
- the initial states


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The semantic model yields a formal representation of:

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- the stepwise behaviour $\longleftarrow$ transitions
- the initial states
- additional information on communication state properties


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The semantic model yields a formal representation of:

- the states of the system $\longleftarrow$ nodes
- the stepwise behaviour $\longleftarrow$ transitions
- the initial states
- additional information on communication $\longleftarrow$ actions
state properties $\longleftarrow$ atomic proposition


## Transition system (TS)

A transition system is a tuple

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\mathcal{T}=\left(S, A c t, \longrightarrow, S_{0}, A P, L\right)
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i.e., transitions have the form $s \xrightarrow{\alpha} s^{\prime}$ where $s, s^{\prime} \in S$ and $\alpha \in A c t$
- $S_{0} \subseteq S$ the set of initial states,
- AP a set of atomic propositions,
- $L: S \rightarrow 2^{A P}$ the labeling function


## Transition system for beverage machine



## Transition system for beverage machine


state space $S=\{$ pay, select, coke, sprite $\}$ set of initial states: $S_{0}=\{$ pay $\}$

## Transition system for beverage machine


actions:
coin
$\tau$
get_sprite get_coke
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state space $S=\{$ pay, select, coke, sprite $\}$
set of initial states: $S_{0}=\{$ pay $\}$
set of atomic propositions: $A P=\{$ pay, drink $\}$
labeling function: $L($ coke $)=L($ sprite $)=\{d$ rink $\}$

$$
L(\text { pay })=\{\text { pay }\}, L(\text { select })=\emptyset
$$

## Transition system for beverage machine


state space $S=\{$ pay, select, coke, sprite $\}$
set of initial states: $S_{0}=\{$ pay $\}$
set of atomic propositions: $A P=S$
labeling function: $L(s)=\{s\}$ for each state $s$

## "Behaviour" of transition systems

possible behaviours of a TS result from:
select nondeterministically an initial state $s \in S_{0}$ WHILE $\boldsymbol{s}$ is non-terminal DO
select nondeterministically a transition $s \xrightarrow{\alpha} s^{\prime}$
execute the action $\alpha$ and put $\boldsymbol{s}:=\boldsymbol{s}^{\prime}$

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reachable fragment:
$\operatorname{Reach}(\mathcal{T})=$ set of all states that are reachable from an initial state through some execution

## Possible meanings of nondeterminism in TS

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- (true) concurrency modeled by interleaving
- competition of parallel dependent actions
- implementational freedom, underspecification
- incomplete information on system environment


## Transition system for parallel actions

parallel execution of independent actions
parallel execution of dependent actions

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\text { e.g. } \underbrace{x:=x+1}_{\text {action } \alpha} \| \mid \underbrace{y:=y-3}_{\text {action } \beta} \quad \alpha, \beta \text { independent }
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parallel execution of independent actions $\leftarrow$ interleaving

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## Implementation freedom

... modelled by nondeterminism

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realization by a TS:


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at a future refinement step the nondeterminism is replaced with one of the alternatives

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realization by a TS:

refined TS:

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## Underspecification

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## Underspecification


at a future refinement step the nondeterminism is replaced with probabilism

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- (true) concurrency modeled by interleaving
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- implementational freedom, underspecification
- incomplete information on system environment, e.g., interfaces with other programs, human users, sensors


## Incomplete information on the environment


mobile phone

## Incomplete information on the environment


mobile phone
resolution of the nondeterministic choices by a human user

## Possible meanings of nondeterminism in TS

concurrency (interleaving)

## $\alpha||\mid \beta$ is represented by


competitions
to be resolved by a scheduler

$$
\text { e.g. } x:=x+1 \| x:=3 x
$$


underspecification, implementational freedom
incomplete information on system environment, e.g., interfaces with other programs, human users, sensors

